

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	125	(CDR or clock near2 recover\$3 or data near2 recover\$3) same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device)	USPA T; US-P GPUB ; EPO; JPO	2004/08/06 17:47	
2	BRS	L2	53	(CDR or data adj2 clock adj2 recover\$3 or clock adj2 data adj2 recover\$3 or data adj2 clock adj2 track\$4 or clock adj2 data track\$3) same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPA T; US-P GPUB ; EPO; JPO	2004/08/06 18:54	
3	BRS	L3	13	(CDR or data adj2 clock adj2 recover\$3 or clock adj2 data adj2 recover\$3 or data adj2 clock adj2 track\$4 or clock adj2 data adj2 track\$3) same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPA T; US-P GPUB ; EPO; JPO	2004/08/06 17:30	

	Error Definition	Er ro rs
1		0
2		0
3		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
4	BRS	L4	16	(CDR or data adj2 recover\$3 with clock adj2 recover\$3 or recover\$3 near3 circuit\$3) same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:37	
5	BRS	L5	23	(CDR or clock near2 recover\$3 or data near2 recover\$3) same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:40	
6	BRS	L6	2780	(CDR or clock near2 recover\$3 with data near2 recover\$3 or recover\$3 near2 circuit\$3) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or phase near2 compar\$5 or phase near2 detect\$4 or frequency near2 compar\$5)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:50	

	Error Definition	Er ro rs
4		0
5		0
6		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L7	16	6 same (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:47	
8	BRS	L8	136	6 and (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:51	
9	BRS	L9	1105	(CDR or clock near2 recover\$3 with data near2 recover\$3) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or phase near2 compar\$5 or phase near2 detect\$4 or frequency near2 compar\$5)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 17:50	
10	BRS	L10	81	9 and (PLD or programmable adj2 logic or programmable adj2 circuit\$3 or programmable adj2 device)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 18:28	
11	BRS	L11	0	6631144.URPN.	USPAT	2004/08/06 18:03	
12	BRS	L12	4	("4641303" "5566180" "5696800" "6047007").PN.	USPAT	2004/08/06 18:04	
13	BRS	L13	2	6295563.URPN.	USPAT	2004/08/06 18:09	
14	BRS	L14	6	("4896337" "5038120" "5517678" "5918073" "6044124" "6058109").PN.	USPAT	2004/08/06 18:09	
15	BRS	L15	1	6292116.URPN.	USPAT	2004/08/06 18:13	
16	BRS	L16	5	("5083119" "5568432" "5848005" "6064599" "6292116").PN.	USPAT	2004/08/06 18:13	
17	BRS	L17	28	5896561.URPN.	USPAT	2004/08/06 18:17	

	Error Definition	Er ro rs
7		0
8		0
9		0
10		0
11		0
12		0
13		0
14		0
15		0
16		0
17		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
18	BRS	L18	11	("4071908" "4503533" "4654656" "4736371" "4987571" "5012469" "5042083" "5070553" "5123029" "5274841" "5355375").PN.	USPA T	2004/08/06 18:18	
19	BRS	L19	9	5838749.URPN.	USPA T	2004/08/06 18:23	
20	BRS	L20	15	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recov\$3 with data adj2 recover\$3) same (PLD or programmable adj2 logic or PLA or logic adj2 array\$1) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback near2 loop\$1 or loop near2 filter\$3 or phase near2 track\$3 or loop\$1 near2 track\$3 or loop near2 recover\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/06 18:35	
21	BRS	L22	181	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recover\$3 with data adj2 recover\$3) and(PLD or programmable adj2 logic or PLA or logic adj2 array\$1) and (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback near2 loop\$1 or loop near2 filter\$3 or phase near2 track\$3 or loop\$1 near2 track\$3 or loop near2 recover\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/06 18:37	

	Error Definition	Er ro rs
18		0
19		0
20		0
21		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
22	BRS	L23	37	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recover\$3 with data adj2 recover\$3) same (PLD or programmable adj2 logic or PLA or logic adj2 array\$1)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 18:51	
23	BRS	L24	989	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recover\$3 with data adj2 recover\$3) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback near2 loop\$1 or loop near2 filter\$3 or phase near2 track\$3 or loop\$1 near2 track\$3 or loop near2 recover\$3)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 18:37	
24	BRS	L25	456	(PLD or programmable adj2 logic or PLA or logic adj2 array\$1) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback near2 loop\$1 or loop near2 filter\$3 or phase near2 track\$3 or loop\$1 near2 track\$3 or loop near2 recover\$3)	USPAT; US-PGPUB; EPO; JPO	2004/08/06 18:38	
25	BRS	L26	25	24 and 25	USPAT; US-PGPUB; EPO; JPO	2004/08/06 18:38	

	Error Definition	Er ro rs
22		0
23		0
24		0
25		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
26	BRS	L21	15	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recover\$3 with data adj2 recover\$3) same (PLD or programmable adj2 logic or PLA or logic adj2 array\$1) same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback near2 loop\$1 or loop near2 filter\$3 or phase near2 track\$3 or loop\$1 near2 track\$3 or loop near2 recover\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:40	
27	BRS	L27	22	23 not 26	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:40	
28	BRS	L28	4	6091261.URPN.	USPA T	2004/08/0 6 18:43	
29	BRS	L29	158	(CDR or data near1 clock\$3 near2 recover\$3 or data near1 clock\$3 near3 circuit\$3 or clock adj2 recover\$3 with data adj2 recover\$3) same (PLD\$1 or programmable adj2 logic or PLA or logic adj2 array\$1 or FPGA or PGA or programmable adj2 array\$1 or combin\$10 near2 logic\$3 or combin\$10 near3 circuit\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:53	

	Error Definition	Er ro rs
26		0
27		0
28		0
29		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
30	BRS	L31	77	29 and (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:54	
31	BRS	L30	32	29 same (PLL or phase adj2 lock\$3 adj2 loop\$1 or feedback adj2 loop or phase near2 detect\$4 or phase near2 compar\$5 or frequency near2 detect\$4 or frequenc\$3 near2 compar\$6)	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:59	
32	BRS	L32	45	31 not 30	USPA T; US-P GPUB ; EPO; JPO	2004/08/0 6 18:59	
33	BRS	L33	0	6400291.URPN.	USPA T	2004/08/0 6 19:07	
34	BRS	L34	9	("4447804" "4620180" "5058141" "5152000" "5357249" "5757294" "5799211" "5930311" "6072843").PN.	USPA T	2004/08/0 6 19:07	
35	BRS	L35	12	5357249.URPN.	USPA T	2004/08/0 6 19:10	
36	BRS	L36	7	("3742199" "4608702" "4675652" "4710922" "4728930" "4751699" "5030951").PN.	USPA T	2004/08/0 6 19:11	

	Error Definition	Er ro rs
30		0
31		0
32		0
33		0
34		0
35		0
36		0